

IN THE CLAIMS

Please amend claims 1 and 3 to read as follows:

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DH
1 (amended twice). A flat panel display apparatus for receiving display
2 information including video data and synchronizing data from a host processing digital
3 data in a serial digital communication, said display apparatus adapted for operation
4 without need for any analog-to-digital converter (ADC) and phase-locked loop (PLL)
5 circuit for signal conversion, said display apparatus comprising:

E1
6 a receiver for reconstructing said display information;

7 a synchronizing signal generator for generating a synchronizing signal by
8 extracting the synchronizing data from said reconstructed display
9 information;

10 a digital-to-analog converter (DAC) for converting said video data to a
11 corresponding analog video signal; and

12 an output terminal for externally transferring said synchronizing signal and analog
13 video signal to an analog display apparatus.

E2
cmf
1 3 (amended twice). A digital data processing system including a host computer for
2 processing digital data and a flat panel display apparatus for displaying display
3 information received from said host computer, said system comprising:

4 a transmitter connected to said host to transfer digital display information as serial
5 data;

6 a receiver for reconstructing said digital display information;

7 a synchronizing signal generator for generating a synchronizing signal by
8 extracting synchronizing data from said reconstructed display information;

9 a digital-to-analog converter (DAC) for converting video data to a corresponding
10 analog video signal; and

11 an output terminal for externally transferring said synchronizing signal and said
12 analog video signal to an analog display apparatus;

13 wherein said flat panel display apparatus includes said receiver, said synchronizing signal
14 generator, and said output terminal; and

15 wherein said flat panel display apparatus does not utilize any analog-to-digital converter
16 (ADC) and phase-locked loop (PLL) circuit for signal conversion.